Notice of References Cited Application/Control No. 10/764,515 Examiner David E. Martinez Applicant(s)/Patent Under Reexamination LEE, CHUN-LIANG Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	Α	US-2002/0026532	02-2002	Maeda et al.	709/250
	В	US-			
	С	US-			
	D	US-			
	Е	US-			
	F	US-			
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Serial and Parallel cable schematics and wiring diagrams, http://www.jspayne.com/io/schematics.html , Copyright 1998
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